

# Understanding how to implement sparse matrix operations efficiently

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#### Overview

#### A talk in three parts

- Matrices
- Why sparse matrices are different, and why this causes problems
- Our plan to improve the situation

Wherever there is a *pattern* (if you can find it)...

...there is (potentially) an optimisation (if you can find it)

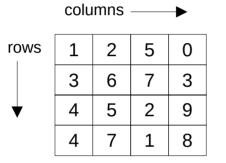
...and this is hopefully how we find it

Don't expect to see any results



#### Matrices

#### Not necessarily square (but often are)



A block of numbers that are treated as a whole, with their own algebra of operations



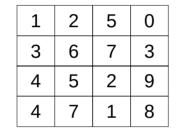
#### Matrices

1	2	5	0
З	6	7	3
4	5	2	9
4	7	1	8

A block of numbers that are treated as a whole, with their own algebra of operations x + 2y +5z3x + 6y +7z +3p4x + 5y +2z + 9p4x + 7y + z + 8p

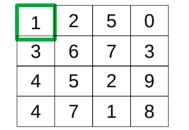
The operations mirror the way that linear equations are handled, with the numbers in the matrix corresponding to coefficients in the corresponding equations





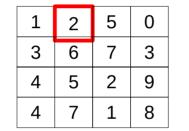
5	2	5	7
5	8	1	2
8	6	3	1
8	3	5	9





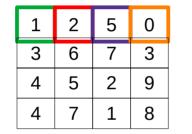
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8	6	3	1
8	3	5	9





5	2	5	7
5	8	1	2
8	6	3	1
8	3	5	9







5	2	5	7
5	8	1	2
8	6	3	1
8	3	5	9

...



Each element in the result matrix is the sum of four multiplications of elements in the argument matrices



#### Patterns

How does this number interact with the numbers in the other matrix?

1	2	5	0
3	6	7	3
4	5	2	9
4	7	1	8

5	2	5	7
5	8	1	2
8	6	3	1
8	3	5	9





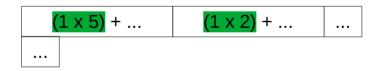
1	2	5	0	
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4	5	2	9	
4	7	1	8	

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5	2	5	7
5	8	1	2
8	6	3	1
8	3	5	9

The numbers in *column* 1 are multiplied by all the numbers in *row* 1

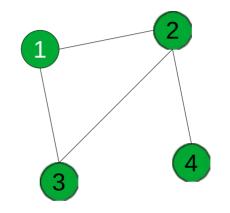




This pattern is independent of the numbers themselves: it's purely structural

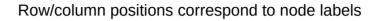


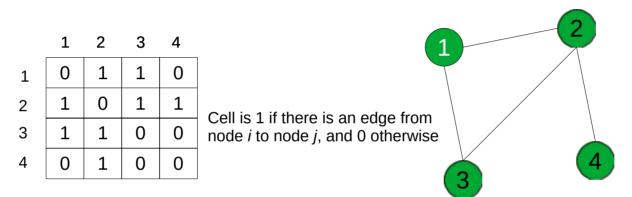
#### Other sources of matrices





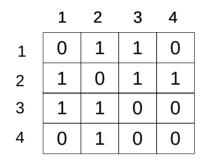
### Other sources of matrices



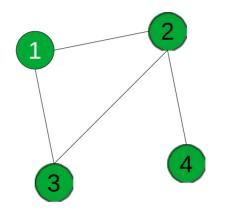


The *adjacency matrix A* corresponding to the graph



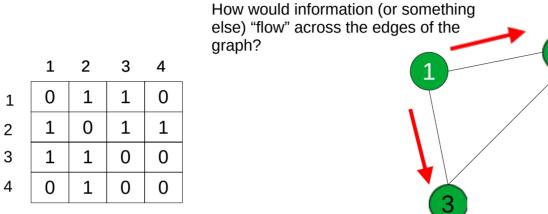


The adjacency matrix captures the *static* features of the graph – its structure



Define an *operator* that maps values at nodes to new values based on the edge structure – the *dynamics* of processes over the graph





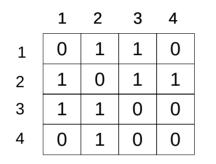
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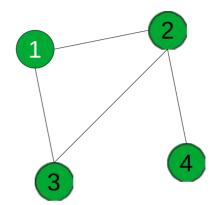
Define an *operator* that maps values at nodes to new values based on the edge structure – the *dynamics* of processes over the graph

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4







L = D - A

	1	2	3	4	_
1	2	-1	-1	0	(
2	-1	3	-1	-1	
3	-1	-1	2	0	
4	0	-1	0	-1	

The *Laplacian matrix L* corresponding to the graph

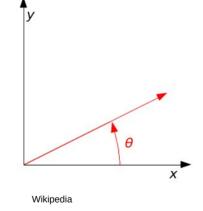
The degree of nodes on the diagonal; -1 where nodes are adjacent; 0 otherwise

In linear algebra "operator" typically (although not necessarily) means "a special matrix derived somehow that we'll then multiply by"





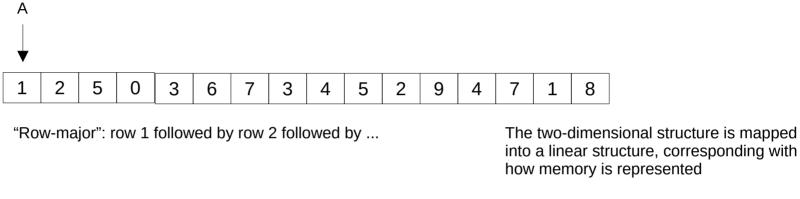
A rotation operator, a constant matrix that takes any point (expressed as a column vector) to that point rotated around the origin by  $\theta$ 



Matrices are closely related to geometry, and any operator has a corresponding (not always obvious, or interesting) geometric analogue



## **Representing matrices**



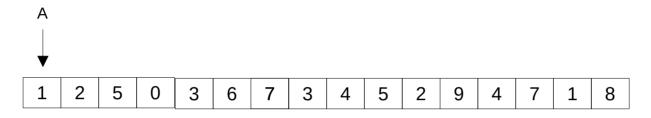
Low address

into a linear structure, corresponding with how memory is represented

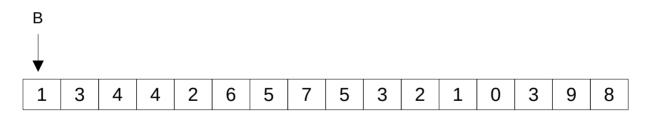
High address



## Representing matrices

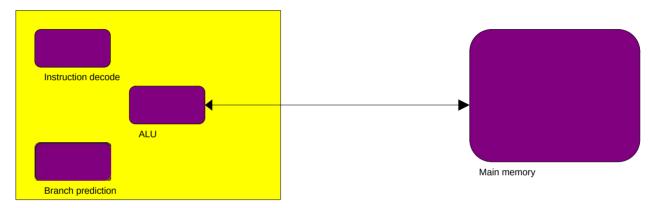


"Row-major": row 1 followed by row 2 followed by ...



"Column-major": column 1 followed by column 2 followed by ...

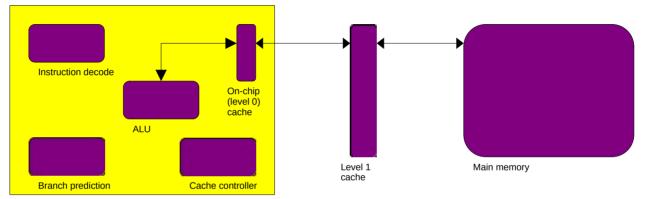




The "von Neumann" model of a processor core: a single CPU interacting with a single block of memory



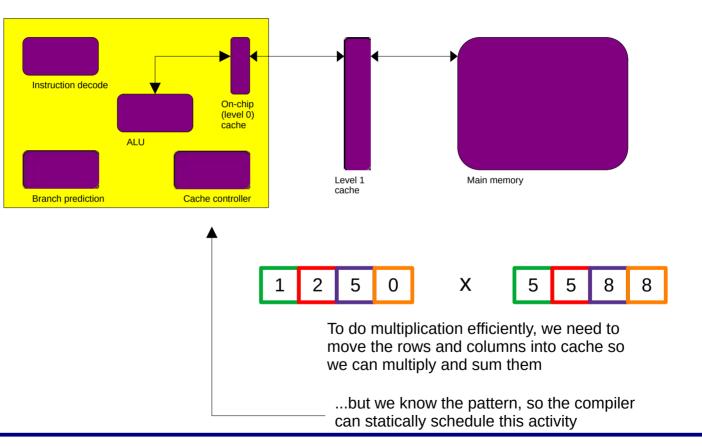
(John von Neumann would probably be horrified to have his name associated with this, but never mind...)



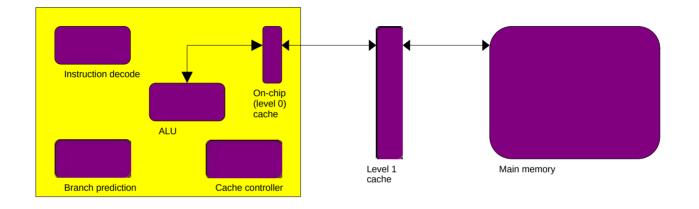
Getting performance means having data in on-chip cache ready to be accessed by the ALU May be several levels of cache, with their own logic and/or API to move data around programmatically between the core and the main memory













Wherever there is a pattern (if you can find it)...

...there is (potentially) an optimisation (if you can find it)

To do multiplication efficiently, we need to move the rows and columns into cache so we can multiply and sum them

...but we know the pattern, so the compiler can statically schedule this activity





1	2	5	0
3	6	7	3
4	5	2	9
4	7	1	8

Our first matrices so far were *dense*: few if any of the values are zero





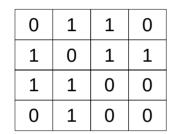
1	2	5	0
З	6	7	3
4	5	2	9
4	7	1	8

0	1	1	0
1	0	1	1
1	1	0	0
0	1	0	0

The adjacency matrix, however, is *sparse*: most of its elements are zero



# Sparse multiplication



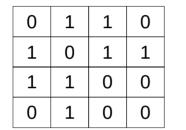
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5	2	5	7
5	8	1	2
8	6	3	1
8	3	5	9

Lots of zero terms in one (or both) argument matrices



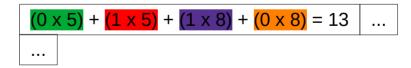
# Sparse multiplication



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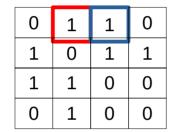
5	2	5	7
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8	6	3	1
8	3	5	9

Several result terms are *necessarily* zero, regardless of the other matrix



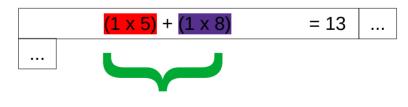


# Sparse multiplication





5	2	5	7
5	8	1	2
8	6	3	1
8	3	5	9



Only do the multiplications that can result in a non-zero result

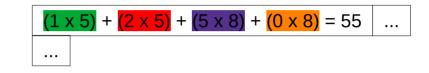


# Sparsity patterns

1	2	5	0
3	6	7	3
4	5	2	9
4	7	1	8

5	2	5	7
5	8	1	2
8	6	3	1
8	3	5	9

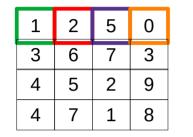
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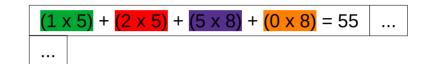
In the dense case the pattern of operations is constant and independent of the actual values in the argument matrices



# Sparsity patterns



5	2	5	7
5	8	1	2
8	6	3	1
8	3	5	9



0	1	1	0	
1	0	1	1	
1	1	0	0	
0	1	0	0	

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5	2	5	7
5	8	1	2
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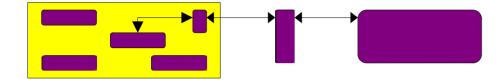
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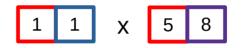
(1 × 5) + (1 × 8) = 13 ...

In the sparse case, by contrast, the multiplications that take place depend critically on the sparsity pattern of one or both argument matrices



# Sparsity and performance





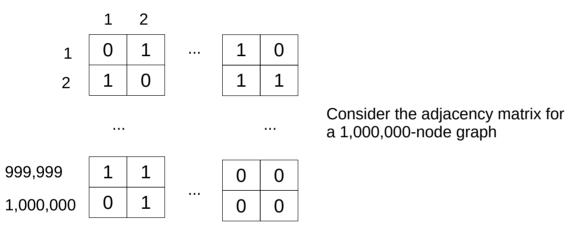
The compiler only knows what to move to which cache with reference to the actual data in the argument matrices

...and we can only get performance when we have the data in cache: if it's in main memory the core can't run at anything approaching full speed

...and we can't know this until run-time

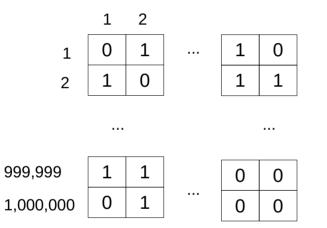


# Sparsity and storage





# Sparsity and storage



If each node has on average 100 neighbours, each row has on average 100 non-zero elements and 999,900 zero elements

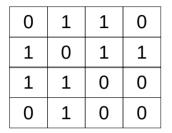
Zero almost everywhere



1	2	5	0
3	6	7	3
4	5	2	9
4	7	1	8

Dense: two representations (row- versus column-major)





Sparse: 11+ representations



0	1	1	0
1	0	1	1
1	1	0	0
0	1	0	0

Sparse: 11+ representations

(((0 1) 1) ((0 2) 1) ((1 0) 1) ((1 2) 1) ((1 3) 1) ((2 0) 1) ((2 1) 1) ((3 1) 1))

Co-ordinate list (COO)

(((1 1) (2 1)) ((0 1 (2 1) (3 1)) ((0 1) (1 1)) ((1 1))) List of lists (LoL)

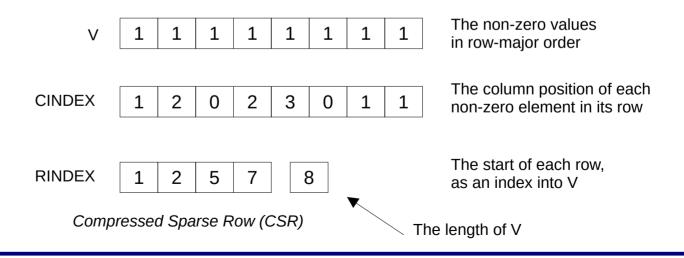


0	1	1	0
1	0	1	1
1	1	0	0
0	1	0	0

Sparse: 11+ representations

(((0 1) 1) ((0 2) 1)
((1 0) 1) ((1 2) 1) ((1 3) 1)
((2 0) 1) ((2 1) 1)
((3 1) 1))
Co-ordinate list (COO)

(((1 1) (2 1)) ((0 1 (2 1) (3 1)) ((0 1) (1 1)) ((1 1))) List of lists (LoL)



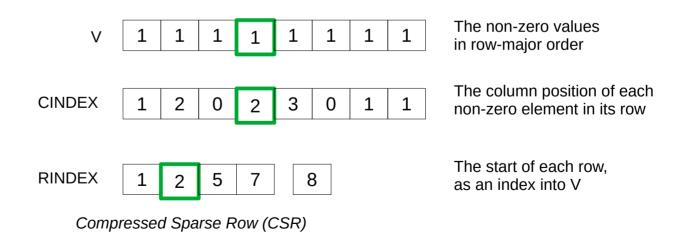


0	1	1	0
1	0	1	1
1	1	0	0
0	1	0	0

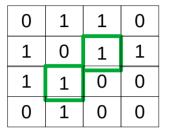
Sparse: 11+ representations

(((0 1) 1) ((0 2) 1)
((1 0) 1) ((1 2) 1) ((1 3) 1)
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((3 1) 1))
Co-ordinate list (COO)

(((1 1) (2 1)) ((0 1 (2 1) (3 1)) ((0 1) (1 1)) ((1 1))) List of lists (LoL)







Sparse: 11+ representations

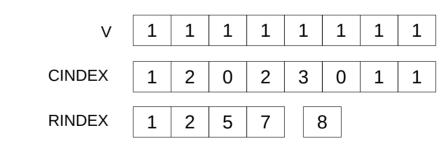
use of the two most obvious facts:

- The matrix is symmetrical  $(A_{ij} = A_{ji})$ 

- All non-zero values are 1

...and none of these representations make

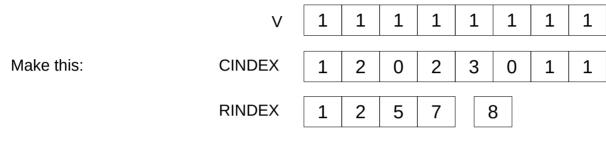
(((0 1) 1) ((0 2) 1) ((1 0) 1) ((1 2) 1) ((1 3) 1) ((2 0) 1) ((2 1) 1) ((3 1) 1)) Co-ordinate list (COO) (((1 1) (2 1)) ((0 1 (2 1) (3 1)) ((0 1) (1 1)) ((1 1))) List of lists (LoL)



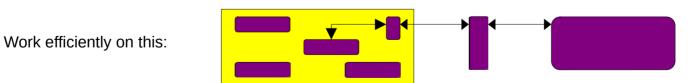
Compressed Sparse Row (CSR)



# Our challenge



Compressed Sparse Row (CSR)





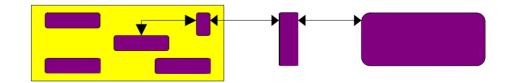
# Our challenge



Compressed Sparse Row (CSR)

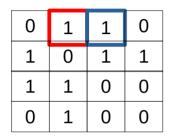
What are the patterns we observe in (sparse) matrix operations that we can use to drive efficient computation?

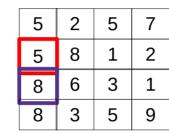
Work efficiently on this:



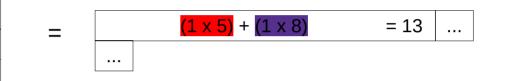


The plan

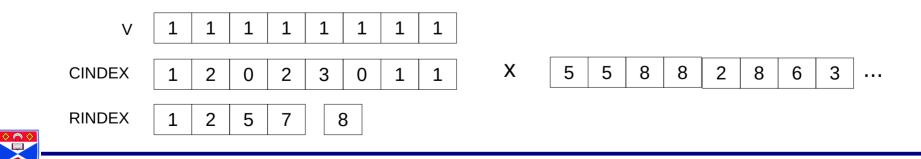




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How do the different representations interact? Are there special cases?





Are there efficient ways of deriving the interesting operators under different representations?



Writing a matrix library that records the basic operations (as well as doing them)

- Gives us a stream of element-wise multiplications and additions Implement the algorithms against (descriptions of) the different representations
  - What sorts of locality of reference are there?
  - Do the special operators generate characteristic patterns of data or operations?

Wherever there is a *pattern* (if you can find it)...

...there is (potentially) an *optimisation* (if you can find it)

...and this is hopefully how we find it

